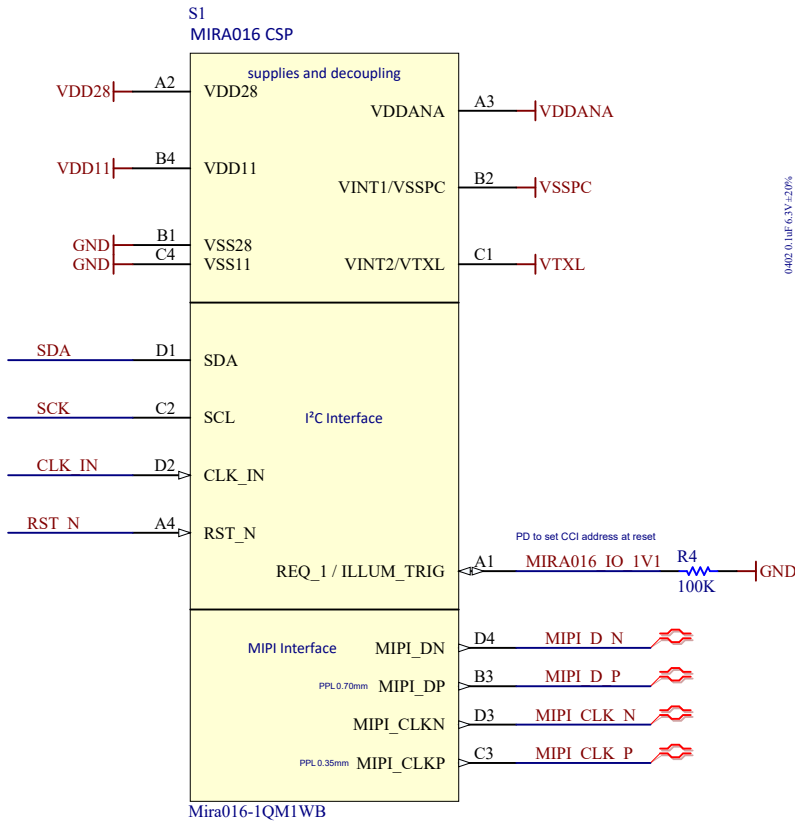
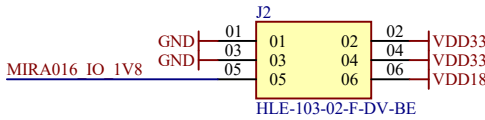
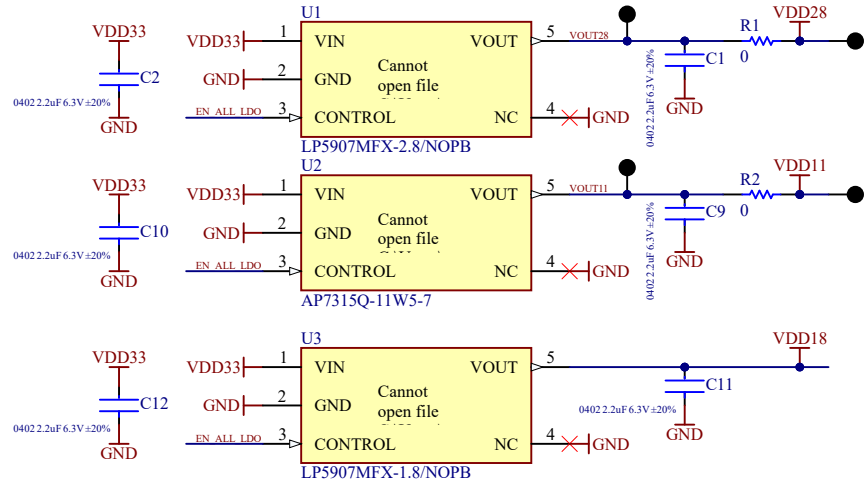
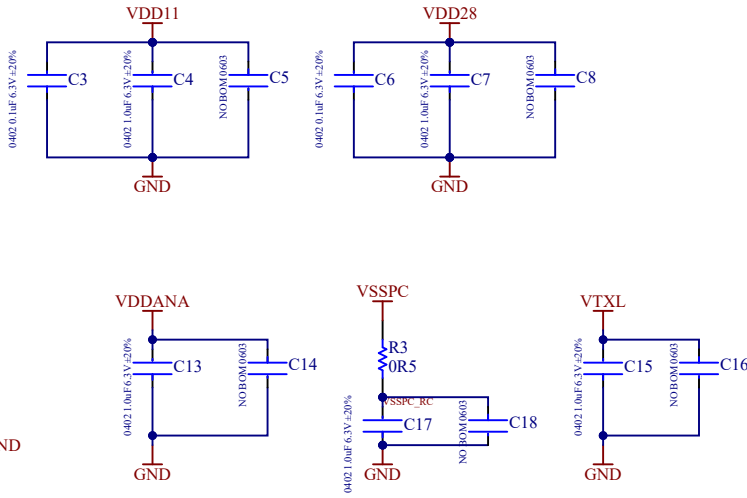


# MIRA016 CSP Reference Design

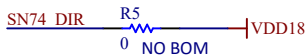
Local power supplies



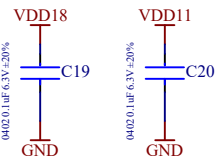
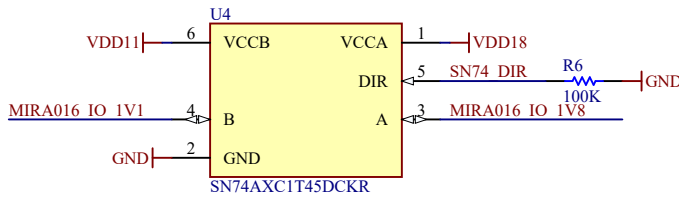
device address 0b011011x  
0x36, 0x37  
default 0b0110110 = 0x36; 0x37 not exposed



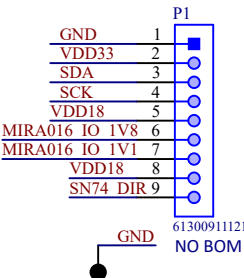
close jumper/resistor for REQ\_EXP input



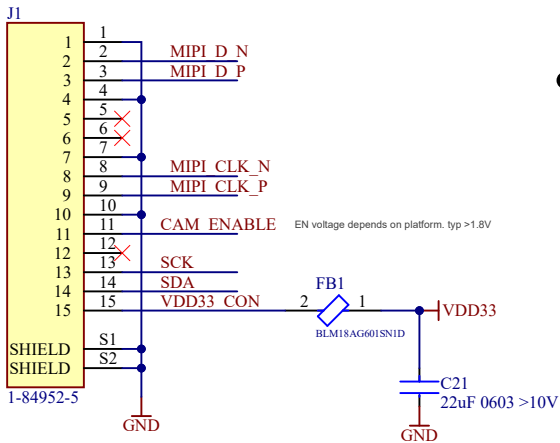
level shifter to 1.8V for MIRA016 single IO



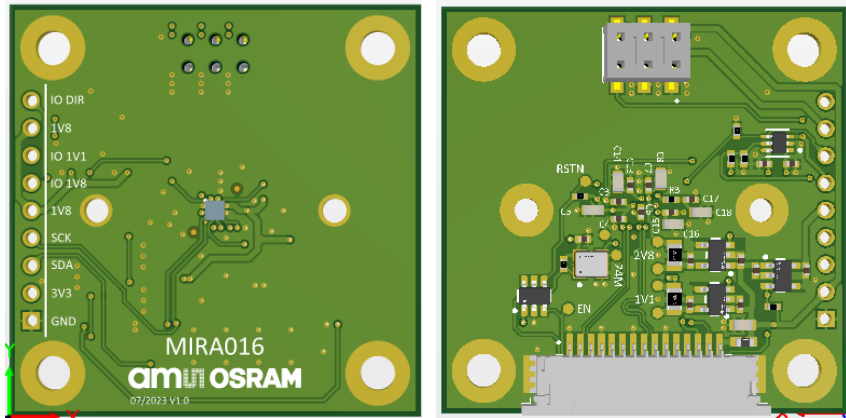
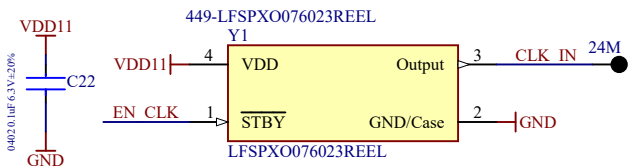
additional sensor I/O



FPC 15pin connector



CLK 24MHz, CMOS, 1.2V-1.1V



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Drawn MKOC	Approved/Checked	Drawing No. *
Title, Comments MIRA016 CSP Reference Design	Project MIRA016	Rev. 1.0
	Date 15-Jun-2023	Sheet 1 / 1

Fid1  
Fid  
Fiducial

Fid2  
Fid  
Fiducial